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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10033110	FILING DATE 10/26/2001	CLASS 708	SUBCLASS 755	GAU 212	EXAMINER Abraham
**APPLICANTS: Wolf Tod; Gatherer Alan; <div style="margin-left: 150px;">714 RSD 2133</div>					
**CONTINUING DATA VERIFIED:					
** FOREIGN APPLICATIONS VERIFIED:					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO TI-33162	
Verified and Acknowledged Examiners's initials TITLE : Using quadrant shifting to facilitate binary arithmetic with two's complement operands <small>U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)</small>					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
ISSUE FEE		Assistant Examiner	
Amount Due	Date Paid	Primary Examiner	
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	
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